

Europass
Curriculum Vitae



Personal information

First name(s) / Surname(s)

Ioan Tudosa

Address(es)

Home address: Str.1 Decembrie, No.107, Bloc B1, Et.4, Ap.16, 715100 Darabani, Botosani ROMANIA;

Work address: San Vittorino Compound – LESIM Laboratory - via Tenente Pellegrini, 82100 Benevento, ITALY.

Telephone(s)

Mobile: RO: +40749914920; IT: +393291845580

E-mail

ioan.tudosa@gmail.com; itudosa@tuiasi.ro; ioan.tudosa@unisannio.it

Nationality

Romanian

Date of birth

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Gender

Male

Occupational field

Research & Development, Teaching

Work experience in ITALIA

2015-2016

Research Assistant, 15.06.2015 – 14.06.2016

- One year research assistant within the project TELEMACO;
- Dipartimento di Ingegneria dell'Università degli Studi del Sannio, per il Settore Scientifico-Disciplinare ING-INF/07 "Misure elettriche ed elettroniche". Codice Assegno B, avente ad oggetto: "**Studio e ricerca sulla caratterizzazione e calibration di un sistema a scansione elettronica del fascio**", di cui al bando n° 85/2015, affisso in data del 05/05/2015.

2012-2014

Postdoctoral Research Assistant

Dates

01.06.2012 – 30.05.2014

Occupation or position held

Research assistant, PhD. Engineer

Main activities and responsibilities

Wireless sensors and actuator networks; intelligent sensors design; embedded systems; distributed measurement systems; teaching and mentoring;

Name and address of employer

University of Sannio,
Department of Engineering
Address: Piazza Guerrazzi, 1
82100 Benevento, Italy
Website: <http://www.unisannio.it>;

Type of business or sector

Research & Education

Website:

Personal bio statement:
<http://lesim1.ing.unisannio.it/index.php/en/team/team/12-lesim-team/13-ioan-tudosa>;

Education and training

Dates

01.10.2008 – 01.12.2011

Title of qualification awarded

Ph.D. in Electrical Engineering

Principal subjects/occupational skills covered
 Title of my Ph.D. thesis is: *RESEARCHES UPON SOME POSSIBILITIES OF QUALITY IMPROVEMENT OF MEASUREMENT SIGNALS* and was supervised by Professor Dr. CRISTIAN FOSALAU. Research interests are related to digital signal processing using digital signal processors and programmable logic circuits, data acquisition systems using virtual instrumentation, embedded systems, modern measurement systems, adaptive signal processing, mathematical algorithms for adaptive digital signal processing using digital signal processors and programmable logic circuits.

Name and type of organization providing education and training
 The "Gheorghe Asachi" Technical University of Iasi, Faculty of Electrical Engineering, Address: Bd. Profesor Dimitrie Mangeron, nr. 51- 53, 700050 IASI, ROMANIA
 Website: <http://www.ee.tuiasi.ro>; <http://www.tuiasi.ro>;

Level in national or international classification
 ISCED 6

Dates
11.02.2010 – 11.05.2010

Title of qualification
research internship in Barcelona, Spain

Principal subjects/occupational skills covered
 Battery management system of an electric/hybrid car simulator developed in MATLAB / Simulink for SEAT Automobile Company in partnership with UPC.

Name and type of organization providing education and training
 Universitat Politècnica de Catalunya (UPC), Vilanova i la Geltrú School of Engineering (EPSEVG), Spain

Level in national or international classification
 ISCED 6

Dates
01.10.2008 - 01.07.2009

Title of qualification awarded
Master degree in Information Systems for Environment Monitoring

Principal subjects/occupational skills covered
 Automatic measurement systems using LabVIEW, Network Communication using LabVIEW, Environmental Quality and Maintenance Management, Virtual Monitoring systems using LabVIEW

Name and type of organization providing education and training
 The "Gheorghe Asachi" Technical University of Iasi, Faculty of Electrical Engineering, Address: Bd. Profesor Dimitrie Mangeron, nr. 51- 53, 700050 IASI, ROMANIA
 Website: <http://www.ee.tuiasi.ro>; <http://www.tuiasi.ro>;

Level in national or international classification
 ISCED 5

Dates
01.10.2003 – June 2008

Title of qualification awarded
Diplomat engineer

Principal subjects/occupational skills covered
 Main field of study: applied informatics in electrical engineering
 Specialization: **Applied Informatics**

Name and type of organization providing education and training
 The "Gheorghe Asachi" Technical University of Iasi, Faculty of Electrical Engineering, Address: Bd. Profesor Dimitrie Mangeron, nr. 51- 53, 700050 IASI, ROMANIA
 Website: <http://www.ee.tuiasi.ro>; <http://www.tuiasi.ro>;

Level in national or international classification
 ISCED 5

Dates
01.10.2003 – June 2007

Title of qualification awarded
Diploma in preparation of education

Principal subjects/occupational skills covered
 Main field of study: **teacher preparation**

Name and type of organization providing education and training
 The "Gheorghe Asachi" Technical University of Iasi, Department for Education Didactical Personnel, Address: Bd. Profesor Dimitrie Mangeron, nr. 67, 700050 IASI, ROMANIA
 Website: <http://www.tuiasi.ro>;

Level in national or international classification
 ISCED 5

Other language(s)

Self-assessment

European level (*)

English

Spanish

| Understanding | | | | Speaking | | | | Writing | |
|---------------|--------------|---------|--------------|--------------------|------------|-------------------|------------|---------|--------------|
| Listening | | Reading | | Spoken interaction | | Spoken production | | | |
| C1 | Proficient | C1 | Proficient | C2 | Proficient | C2 | Proficient | C1 | Proficient |
| B1 | Middle level | B1 | Middle level | B1 | Basic | B1 | Basic | B1 | Middle level |

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|---------|----|--------------|----|--------------|----|--------------|----|--------------|----|--------------|
| Italian | B1 | Very good | B1 | Very good | B1 | Middle level | B1 | Middle level | B1 | Middle level |
| German | B1 | Middle level | B1 | Basic User | B1 | Basic User | B1 | Basic User | B1 | Basic User |
| French | B1 | Middle level | B1 | Middle level | B1 | Middle level | B1 | Middle level | B1 | Middle level |

(*) *Common European Framework of Reference for Languages*

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| Social skills and competences | Good ability to adapt to multicultural teams. I'm a sociable person, with self-confidence, communicative, conscientious, team orientated or self organized, responsible and a practical person. |
| Organizational skills and competences | Well organized and multitask person, Leaderships (regular, I have been responsible for a team of 3÷5 people); Good analysis and research skills. |
| Technical skills and competences | <ul style="list-style-type: none"> o Practical experience with microcontrollers (Atmel, PIC, FreeScale); o Practical experience in building and debugging electronic boards; o Analog and digital interfacing to: sensors, transducers, microcontrollers, drivers; o Practical experience with measurement equipment (oscilloscope, multimeters, spectrum and signal analyzer, function generator, PXI system); o Filter design: analog, digital and digital adaptive filter (main scope of Ph.D. Thesis); o RF experience (XBee module from Maxtrim) using IEEE 802.15.4 – ZigBee standard o Computer architecture knowledge (experience in teaching one semester the Laboratories of Computer Architecture course from the Faculty of Electrical Engineering at the 3rd year curricula, 2011-2012); o Ability to design and conduct experiments and ability to analyze and interpret experimental data; o Ability to apply knowledge of mathematics on science and engineering; o Ability to design a system component to meet desired needs. |
| Other Technical skills and competences | <ul style="list-style-type: none"> o Designing applications using National Instruments (NI) hardware (PXI controllers, DAQs Cards, functions generators, scopes, etc.); o Circuit design and practical implementation using OrCAD: <ul style="list-style-type: none"> o Library Manager o Capture (Schematics – Analog and Mixed A/D) o Layout (1 or 2 layers) o PSpice (advanced analog circuit simulations) |
| Computer skills and competences | <ul style="list-style-type: none"> o C/C++ programming language; o Digital design and simulation using Verilog HDL using: Altera: MaxPlusII, Quartus II, NiosII; Xilinx: ISI WebPACK; MentorGraphics: ModelSim; o MATLAB/Simulink – signal processing toolboxes, SimPower Systems and Simscape toolboxes, DSP & FPGA Simulink tools, Real-Time Workshop and Real-Time Workshop Embedded Coder; o Designing architectural LabVIEW applications (middle level to upper level), good knowledge of programming and testing LabVIEW based applications; o Analog circuit simulations using OrCAD PSpice, TinaTI, Proteus; o Strong knowledge of Microsoft Office™ tools. |
| Other skills and competences | Hobby: Physics string theory, Cooking, Music, Movies |
| Additional information | |
| Awards: | 2 nd prize with „Tire Information System” project in the “Open Doors Day” competition - 2 nd Edition 2008 – Siemens VDO Automotive Iasi (A Continental Corporation Company). |
| University R&D projects – member: | “Barriera Attiva”: ideation and processing of a new innovative barrier based on an innovative concept of safety combined with structural function (passive function) and active function; Project director: Prof. Pasquale Daponte Web-link: http://www.barrieraattiva.unisannio.it Position in the project: member of the project team. |

Autoclusters : develop a network cluster for the automotive industry.
Project director: Assoc.Prof. Cristian-Gyozo Haba
Web-link: <http://www.autoclusters.eu>
Position in the project: member of the project team.

"FreeScale your FreeMind": development of learning content and educational kits for automotive applications based on Freescale uCs – project in collaboration with Continental VDO (former Siemens VDO);
Project director: Assoc.Prof. Cristian-Gyozo Haba
Position in the project: member of the project team.

"Nanowires based microtransducers (NANOSENS)": development of a new generation of microtransducers based on magnetic homogenous or multilayer nanowires;
Project director: Assoc.Prof. Cristian Zet
Web-link: <http://www.nanosens.tuiasi.ro/en/index.html>
Position in the project: member of the project team.

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| Professional membership | IEEE Member – member no. 90784648 IEEE Signal Processing Society, since February 2010 IEEE Communications Society, since September 2011 |
| Driving license | B category |
| Papers/Books | Published scientific papers, over 35; Published books/manuals: one as single author, 2011; one as coauthor, 2006; |

Data:
15.06.2016

Ioan Tudosa

